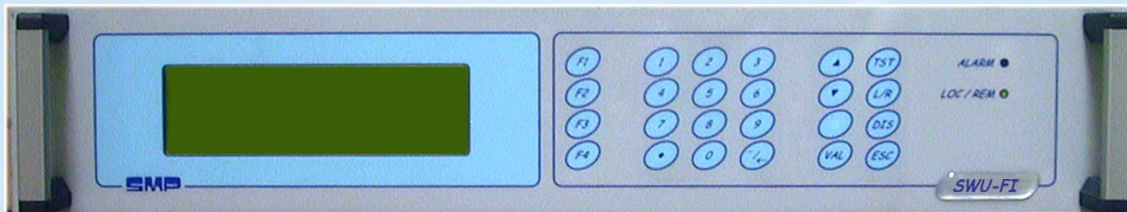

ECL SWITCHING MATRIX

MODEL : SW2U ECL xx*yy



The ECL Switching Matrix provides the selection on x output channels among y input channels. Each input/output channel contains DATA and CLOCK. The electrical interfaces are unbalanced or differential for inputs and outputs and can use BNC, Twin BNC, Twinax or other connector on request.

x and y must be defined at the order as high as 8. It is possible to select any input to any output. The same input can be linked to several outputs, but one output cannot be connected to several inputs at the same time.

The unit can be controlled either in local mode from the front panel or in remote mode via RS232 interface (IEEE 488, Ethernet,... other available as option).

MAIN FEATURES

- High bit rate (> 350 Mbps)
- B.E.R. > 10^{-9}
- ECL interface (50 Ω / 124 Ω), unbalanced – differential
- BNC / TWINBNC / TWINAX